ONF CONNECT DECEMBER 2018

Leveraging Stratum and Tofino Fast Refresh for Software Upgrades

Antonin Bas

Software Engineer, Barefoot Networks

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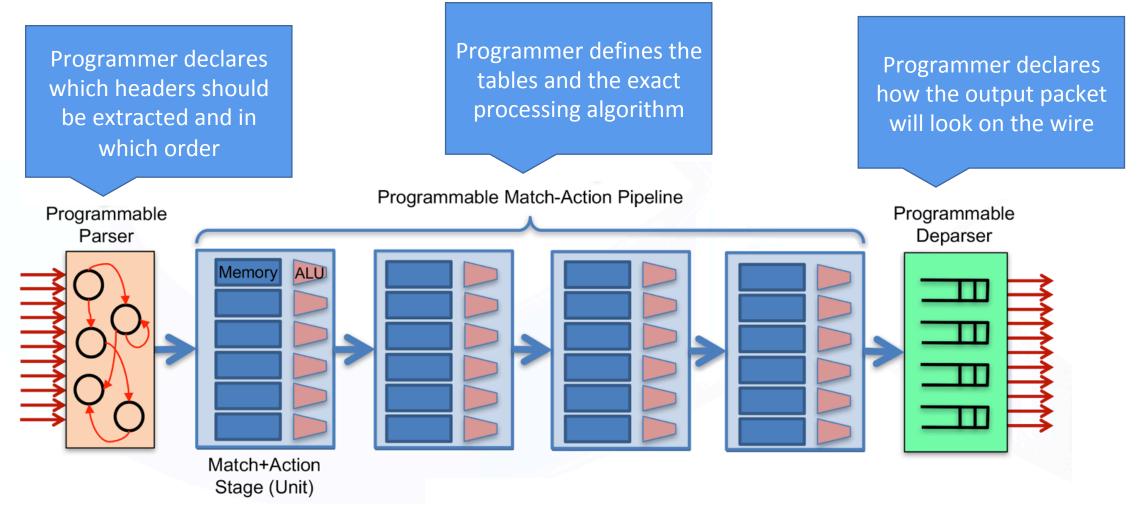


Agenda

- Introduction to Tofino and programmability
- Synergy between Tofino & Stratum
- Current Tofino support for Stratum
- What is Tofino Fast Refresh and why use it?
- Demo: using Fast Refresh to change the switch role and optimize for latency & power

PISA: Protocol Independent Switch Architecture

Abstract machine model of a high-speed programmable switch architecture



What is Barefoot Tofino?

- The first end-user programmable high speed Ethernet switch ASIC
- Modeled after the PISA architecture
- P4 programmable
 - Ships with a P4 compiler
 - If it compiles, it runs at line rate
- 65 x 100Gbps and several smaller SKUs
- No compromise on power consumption & speeds compared to fixed-function ASICs
- Integration with several existing Network Operating Systems, including Stratum!

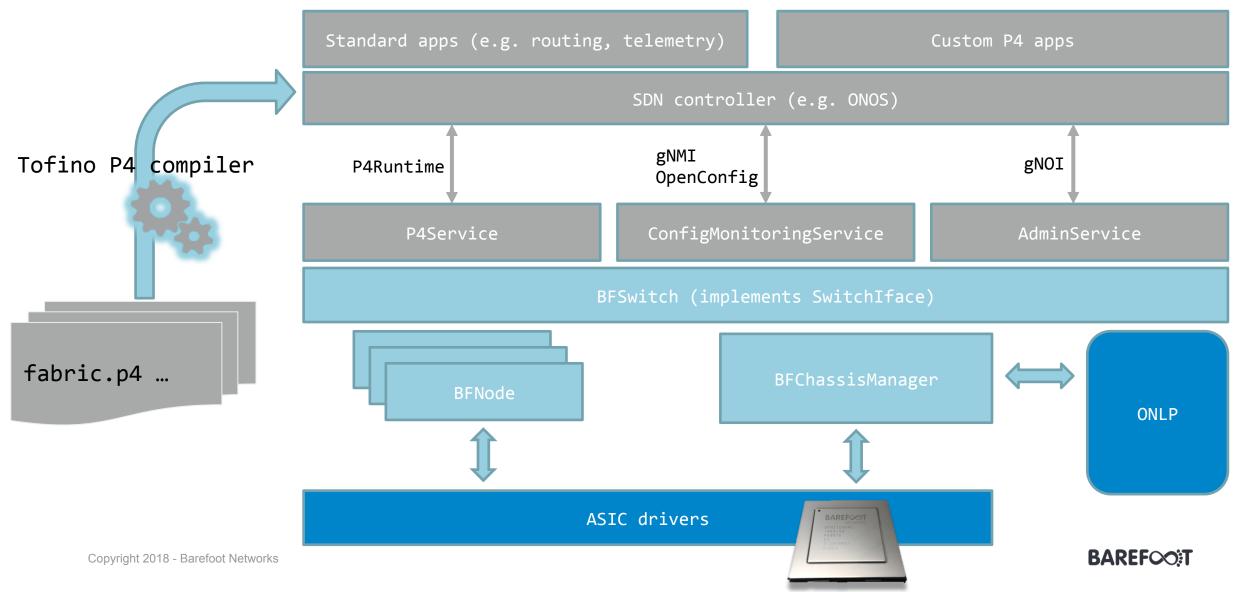
Why data-plane programming?

- **New features:** Realize your beautiful ideas very quickly
- 2. <u>Reduce complexity</u>: Remove unnecessary features and tables
- 3. Efficient use of H/W resources: Achieve biggest bang for buck
- 4. Greater visibility: New diagnostics, telemetry, OAM, etc.
- 5. <u>Modularity</u>: Compose forwarding behavior from libraries
- 6. **Portability:** Specify forwarding behavior once; compile to many devices
- 7. Own your own ideas: No need to share your ideas with others

"Protocols are being lifted off chips and into software" – Ben Horowitz



Tofino support in Stratum



Current status of Tofino support

- Supported today
 - Most of P4Runtime features
 - Packet IO
 - Match-action programming (direct & indirect)
 - All standard externs (counters, trTCM meters, learning, ...) save for stateful registers
 - Port operational status and port stats for gNMI Set & Subscribe
- Upcoming support (Q1 2019)
 - Port configuration through gNMI
 - P4Runtime stateful register support

Why use Tofino with Stratum?

Tofino is the best fit for Stratum

- Most feature-complete & compliant P4Runtime implementation
 - 18+ months of development
 - First demo @ SDN NFV World Congress October 2017
 - Support for advanced features such as dynamic reconfiguration and "rollback-on-error" batch semantics
- Tofino's "native" support for P4 enables high-performance P4Runtime implementation
 - Up to 100,000 new flow rules per second using batching
- Barefoot is an active contributor to Stratum and is committed to keep releasing code and open-sourcing top-level SDK interfaces

What is Tofino Fast Refresh?

A fresh start for your data plane

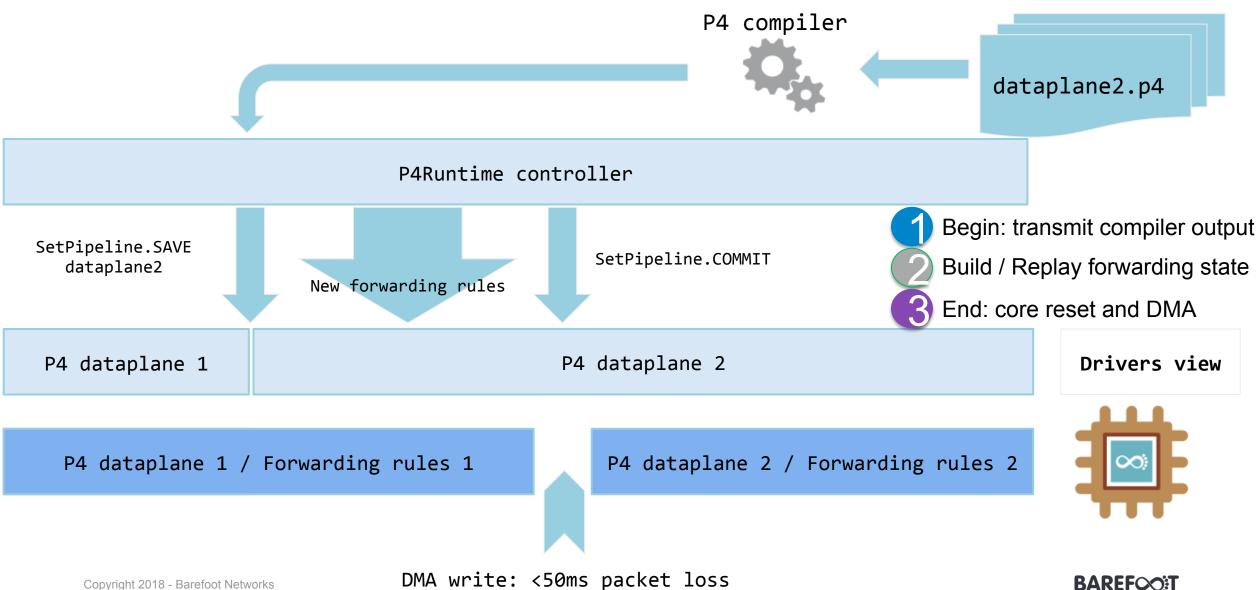
- Reset your switch state: start from a clean slate (new or same P4)
- Simple 3 step sequence:
 - 1. Begin: P4 compiler outputs are given to the drivers
 - 2. Forwarding state is built / replayed through usual API calls
 - 3. End: Drivers are told to perform a core reset and all memories (including forwarding state) are written through batched DMA
- Minimal traffic interruption during step 3: < 50 ms for any P4 program and any set of flow rules
- Facilitated by a new generation of program-independent APIs: P4Runtime, Barefoot Runtime Interface (BRI)
- Can be leveraged by all Network Operating Systems!



Why use Tofino Fast Refresh?

- Unified and resilient mechanism to upgrade software
 - Upgrade data plane, driver stack, control plane or even Linux OS
 - "Bug-free" upgrade: data plane & forwarding state re-built from scratch
- Use for scheduled maintenance & to solve mysterious data plane issues: just refresh it!
- Also use to **change the role** of the switch by reconfiguring it to use a new P4 program (*aka* "Fast Reconfig").
 - Optimize your data plane for a specific feature set
 - Optimize your data plane for low latency or power consumption
- Support multiple data plane profiles and upgrade scenarios in your NOS!

Fast Refresh with P4Runtime



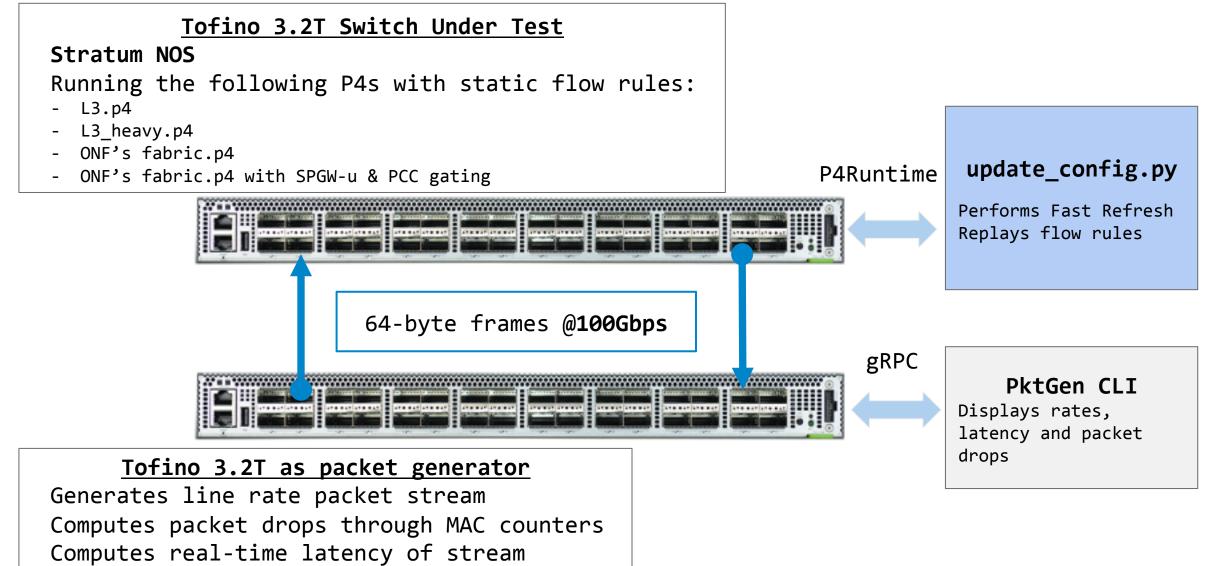
Demo: Power and latency saving by reducing complexity

Using Fast Refresh to run 4 different P4 programs on Tofino

| P4 program | P4 architecture | # MA entries | Description | |
|-------------------------|-----------------|--------------|---|--|
| I) fabric-spgw.p4 (ONF) | PSA | 262,396 | ONF's fabric.p4 with SPGW-u offload and PCC gating 120K on-chip subscriber connections 4K arbitrary IPv4 prefix routes 100K IPv4 host routes | |
| II) fabric.p4 (ONF) | PSA | 113,824 | ONF's fabric.p4 without SPGW-u offload 4K arbitrary IPv4 prefix routes 100K IPv4 host routes | |
| III) L3.p4 | TNA | 277,824 | 7,824 Simple L3 IPv4 forwarding 12K arbitrary prefix routes 200K host routes 65K next hops | |
| IV) L3_heavy.p4 | TNA | 1,343,744 | Heavy L3 IPv4 forwarding • 1M+ host, /28, /24, /20, /16, /8 routes | |

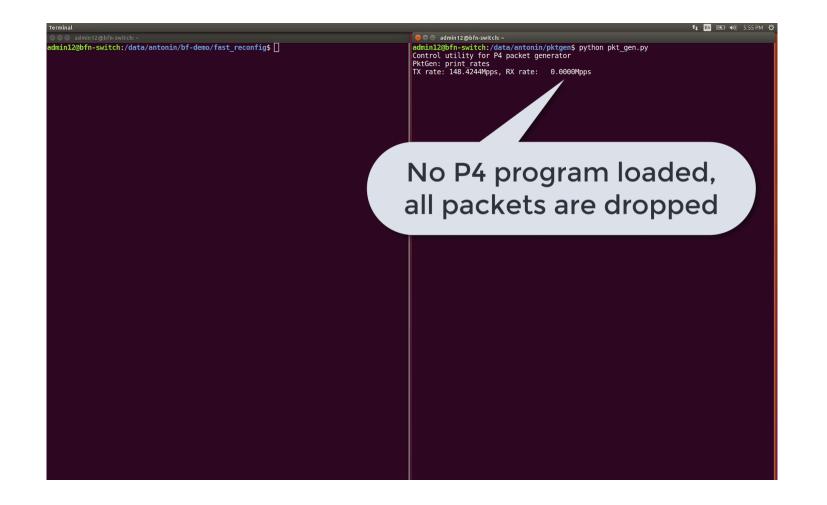


Fast Refresh Demo Setup





Demo video





Demo: Power and latency saving by reducing complexity

Demo results

| P4 program | # MA entries | Measured latency | Estimated worst-case power usage (MA pipeline only) | Packet drop during Fast Refresh |
|-------------------------|--------------|------------------|--|------------------------------------|
| I) fabric-spgw.p4 (ONF) | 262,396 | 681 ns | 53.1% | |
| II) fabric.p4 (ONF) | 113,824 | 644 ns | 27.8% 47.7% savings compared to I) | < 31 ms |
| III) L3.p4 | 277,824 | 370 ns | 9.6% | < 31 ms |
| IV) L3_heavy.p4 | 1,343,744 | 365 ns | 17.8% | < 31 ms |

Takeways

- Use Fast Refresh on Tofino to update P4 programs, upgrade software and reconcile state with minimum traffic interruption
 And stay within SLA!
- Change your P4 program without modifying any x86 code on the switch thanks to program-independent APIs (P4Runtime, BRI)
- Use Fast Refresh in Stratum, SONIC, ...
- Optimize your program for specific features, or for latency / power
- Power of programmability: use Tofino as a packet generator to evaluate another switch!

Thank You

Antonin Bas

antonin@barefootnetworks.com

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